

dX3 400G 8-port Appliance

QSFP-DD Test Platform

400/200/100/50/40/25/10G

Network bandwidth needs continue to grow at a rapid pace. Network equipment manufacturers are developing highly flexible multi-rate products to support the latest generation of HSE devices. Service Providers and Hyperscale data centers are deploying multi-rate networking infrastructure solutions to meet this growing market.

With these multi-rate requirements, customers demand higher density test equipment. Flexibility is needed to validate the next generation of routers and data center fabrics.

The dX3 400G Appliance for VIAVI TestCenter was developed to meet these specific needs with its industry-leading 2x density advantage for QSFP-DD from nearest competitor. VIAVI QSFP-DD platform is a 1U rack mountable appliance and can be configured to support per port (PAM4) 400/200/100/50G and (NRZ) 100/50/40/25/10G encoding. The appliance also supports Auto Negotiation and Link Training for all speeds including 8x50G AN/LT. Trade-in programs are available for customers interested in upgrading existing hardware to support Auto Negotiation and Link Training and NRZ encoding.

The dX3 Appliance also supports VIAVI Smart Port Technology, a licensed feature that allows single port and speed upgrades for maximum value and flexibility. A high-density 100G mode option is also available allowing support for 4xQSFP28 from a single QSFP-DD port, achieving a maximum of 32x100G ports from a single 1U platform.

Key Features

- 8x400G ports per 1U high appliance, delivers the highest density high speed Ethernet solution
- Each port supports following:
 - PAM4: 1x400G, 2x200G, 4x100G 8x50G
 - NRZ: 2x100G, 4x50G, 2x40G, 4x25G, 4x10G (optional licenses may be required to support NRZ encoding)
- Optional 4-port versions
- Available single port upgrades
- High density 32xQSFP28 port option (requires external accessory and license)
- Support for Ethernet (FEC), and Auto Negotiation (AN) and Link Training (LT) for all supported speeds and full breakouts including 8x50G AN/LT
- Support for MACsec across all port speeds
- Protocol testing for L2/3 routing/switching and data center test cases

Key Benefits

- Industry's highest density QSFP-DD test appliance
- PAM4 and NRZ solution in one platform
- Provides large capacity testing for a variety of services
- 4- to 8-port upgrade available via licensing



Applications

Cloud Computing/Streaming Services: Validate data plane QoS on thousands of flows at line rate and test complex routing, data center and access protocols on switches and routers.

Data Center ToR and EoR Switches and Fabrics: Validate forwarding performance, latency, MAC capacity and functional capabilities of ultra-high-scale, next-generation enabled multi-terabit cloud data center fabrics. This platform will allow synchronized timing of 255 systems with no requirement for external timing devices or specialized cabling.

Terabit Routers: Test latest generation of core routers with high-scale, multiprotocol topologies.

Productivity

- Accurate Results: Purpose-built hardware delivers repeatable test execution and precise statistics
- User definable Health Indicator views provide real-time health monitoring and error isolation capability that allows engineers to accurately and quickly pinpoint errors, even in the most complex test configurations. Customizable Time Series charts, overlaid with Events, provide correlation between real-time metrics and system events, allowing rapid debugging of problems and accelerating development
- High performance database underneath a modern web UI processes billions of real-time results to validate tests, identify problems, and provide customizable reports
- Delivers more results with tight correlation, and more information to find those obscure bugs. With more coverage and more information, VIAVI answers questions faster, and in a single test run, where multiple runs are necessary with other test tools
- Interesting streams uses real-time results data mining to dynamically filter through mountains of data and display the results that matter
- Powerful automation with Command Sequencer (Visual Programming) and GUI to Script empowers the test operator to:
 - Construct sophisticated, stressful, automated test cases without programming experience
 - Combine numerous individual test cases into a single run to save regression test time
 - Develop a catalog of broad automated test cases in a fraction of the time
 - Export automated test cases to run from a command line for headless test execution that can be integrated with any automated regression system
- Extensive, Flexible Reporting: Real-time statistics for critical variables across all protocols.

Technical Specifications

dX3 400G Appliance	
Part Number	Speed
DX3-QSFP-DD-8-825A	PAM4 - 400/200/100/50G; NRZ - 100/50/40/25/10G
DX3-QSFP-DD-8-750A	400/200/100/50G
DX3-QSFP-DD-8-700A	400/200/100G
DX3-QSFP-DD-8-550A	400/100/50G
DX3-QSFP-DD-8-400A	400G only
DX3-QSFP-DD-8-350A	200/100/50G
DX3-QSFP-DD-4-825A	PAM4 - 400/200/100/50G; NRZ - 100/50/40/25/10G
DX3-QSFP-DD-4-750A	400/200/100/50G
DX3-QSFP-DD-4-400A	400G only
Product Feature	Description
MSA Interface	QSFP-DD
Operational Modes	PAM4 - 400/200/100/50G; NRZ - 100/50/40/25/10G
Port CPU	Stackable multi-core CPU
User Reservation	Per port
Test Port Speed Configuration	8 test port speed groups per rack mount unit
Line Clocking and Packet Time-Stamping	<p>Stratum-3 rated oscillator is the default time source. Transmit line clock is at the precise nominal Ethernet rate $\pm < 1$ PPM on initial shipment. Accurate to ± 4.6 PPM 15 years of operation</p> <ul style="list-style-type: none"> • Frame time-stamp resolution of 2.5ns • GPS and CDMA-based external time sources are supported • IEEE 1588v2 and NTP packet-based external time sources are supported • TIA/EIA-95B-based external time sources are supported
Appliance Time Synchronization	<p>Appliance Features</p> <ul style="list-style-type: none"> • VIAVI-patented self-calibrating inter-chassis timing chain using dedicated port on chassis control, Appliance delivers precise synchronization ± 20ns • Ability to daisy chain up to 255 appliances for large density testing • Synchronization via external GPS or CDMA network • Using IEEE 1588 or NTP packet-based approaches • With TIA/EIA-95B timing inputs

Technical Specifications continued

Product Feature	Description
Operating Temperature Range	Supported for 41° to 86° F (5° to 30° C) ambient temperature. 20% to 80% relative humidity.
Max Power Draw	Maximum of 1600W per rack mount
Product Dimensions	92.62 cm L x 43.4 cm W x 4.28 cm H (1U)

VIAVI TestCenter Layer 2-3 Generator and Analyzer

Number of Streams	<ul style="list-style-type: none"> • Stats/Streams @400/200/100/50/40/25/10G: Tx=8K, Rx=16K • Stream fields can be varied to create billions of flows
Frame Transmit Modes	Port-based (rate per port), stream-based (rate per stream), burst, timed
Min/Max Frame Size (w/CRC)	60 to 16,004
Min/Max Tx Rates	1 packet per 3.43 seconds to 101% of line rate
Real-Time Tx Stream Adjustments	Change rate and frame length settings without stopping the generator or analyzer for truly interactive, cause and effect analysis
Per-Stream Statistics Analyzed in Real Time	<ul style="list-style-type: none"> • Tx and Rx frame counts and rates • Tx and Rx Layer 1 byte counts and rates • Out of sequence errors • FCS errors and rate • Min, Max and Average Latency (4K streams) • Real Time Dropped Frame count
Per-Port Statistics Analyzed in Real Time	<ul style="list-style-type: none"> • Tx and Rx frame counts and rates • Tx and Rx Layer 1 byte counts and rates • Out of sequence errors • PRBS errors • FCS errors and rate
Transmit Timestamp Resolution	2.5 ns Tx timestamp resolution with intra-chassis and inter-chassis synchronization
Supported Encapsulations	<ul style="list-style-type: none"> • Layer 2: Ethernet II, 802.1Q, 802.1ad, FCoE • Layer 3/4: IPv4, IPv6, TCP, UDP
Supported Tx Signature Capability	Fully compatible with VIAVI hardware; contains sequence number and highly accurate timestamp
Capture Buffer Size	8 MB per port

Technical Specifications continued

Product Feature	Description
<p>Capture Buffer Controls— VIAVI TestCenter’s unique capture capability allows maximum effectiveness when debugging hard to find hardware or protocol problems</p>	<p>Several modes of operation that include: Filter by protocol fields, filter by byte offset and range; store slices or full-frames; store signature or all frames; store Tx/Rx control plane with data plane; real-time mode for control plane traffic; wrap or stop buffer at end. User defined pattern definitions can logically combine 8 filters of up to 32 total bytes. Patterns can be applied to start, filter (quality) or stop capture.</p> <p>In addition to user-patterns, filtering, starting and stopping capture contains the following pre-defined events: FCS, PRBS, IPv4 checksum, TCP/UDP/IGMP checksum, and sequence errors; undersize, oversize, jumbo, and user-defined frame length; IPv4, IPv6, TCP, UDP and IGMP packets; test signature present and test stream ID match. Each event can be independently set to ignore, include or exclude.</p>
<p>Latency Modes</p>	<p>Benchmark tests support LIFO, LIFO, FIFO or FILO latency calculation methods</p>
<p>Route Insertion Table (RIT) Entries per Port</p>	<p>8K 4-byte entries for dynamic label or random IP/MAC address assignments</p>
<p>RIT or List VFD Entries per Stream</p>	<p>8 RIT insertions per stream and 4 VFD insertions per stream</p>
<p>Layer 1 Functionality</p>	
<p>QSFP Interconnects</p>	<p>CR, SR, LR, FR, DR, PSM4, ZR, and ZR+ at multi-rate (400/200/100/50/40/25/10G) NOTE: For transceivers that consume more than 20 watts of power, please consult the factory for additional information before using these optics in the system.</p>
<p>Media support and FEC options</p>	<p>Support varies by module speed mode</p> <ul style="list-style-type: none"> • 400G: 400GBASE-SR8, 400GBASE-DR4, 400GBASE-FR4/FR8, 400GBASE-LR4/LR8, 400GBASE-ZR and ZR+ plus additional MSA PMDs • 200G: 200GBASE-SR4, 200GBASE-PSM4, 200GBASE-LR/FR4, plus additional MSA PMDs • 100G: 100GBASE-SR2, 100GBASE-LR2, 100GBASE-DR2 plus additional MSA PMDs • RS-544 (KP4) FEC all speeds • Direct Access Copper breakouts • NRZ support varies by module speed mode and license • 100G: 100GBASE-SR4, 100GBASE-CR4, 100GBASE-LR4, plus additional MSA PMDs • 50G: 25/50G Consortium 50GBASE-CR2, • 40G: 40GBASE-SR4, 40GBASE-CR4, 40GBASE-LR4 • 25G: 802.3by 25GBASE-CR, 25GBASE-CRS, 25GBASE-SR • 10G: 10GBASE-SR, 10G Copper DAC • QSFP28 to SFP28 breakout cable options • Clause 74 BASE-R FEC, Clause 91 RS-FEC, and Clause 108 RS-FEC • 25/50G Consortium 50GBase-R FEC CL74, 25/50G Consortium 50GBase RS-FEC CL91 • IEEE 25GBASE CR CL74, CL108, CR-S CL74, SR FEC CL108 • 25/50G Consortium 25GBase-R FEC CL74, 25/50G Consortium 25GBase RS-FEC CL91

Technical Specifications continued

Product Feature	Description
AN/LT (Enable/Disable)	Direct Attach Copper (DAC), AN/LT supported for all speeds including 8x50G mode
Layer-1 Debug Tools & Features	CR Tx Emphasis settings, Rx Eye view, FEC Counters, PRBS Gen/Check, Front-end L1 Summary Status, Xcvr MDIO access, PCS monitoring, PCS skew, FEC error injection, PCS random error injection

Ordering Information

Part Number	Description
Base Package Bundle	
DX3-QSFP-DD-8-825A	dX3 400/200/100/50/40/25/10G QSFP-DD 8-Port
DX3-QSFP-DD-8-750A	dX3 400/200/100/50G QSFP-DD 8-Port
DX3-QSFP-DD-8-700A	dX3 400/200/100G QSFP-DD 8-Port
DX3-QSFP-DD-8-550A	dX3 400/100/50G QSFP-DD 8-Port
DX3-QSFP-DD-8-400A	dX3 400G Only QSFP-DD 8-Port
DX3-QSFP-DD-8-350A	dX3 200/100/50G QSFP-DD 8-Port
DX3-QSFP-DD-4-825A	dX3 400/200/100/50/40/25/10G QSFP-DD 4-Port
DX3-QSFP-DD-4-750A	dX3 400/200/100/50G QSFP-DD 4-Port
DX3-QSFP-DD-4-400A	dX3 400G Only QSFP-DD 4-Port
Hardware Upgrades (available as add on after purchase of initial base package bundle)	
HWO-DX3-QSFP-DD-8-400G	400G Hardware Speed Option for dX3-QSFP-DD-8
HWO-DX3-QSFP-DD-8-200G	200G Hardware Speed Option for dX3-QSFP-DD-8
HWO-DX3-QSFP-DD-8-100G	100G Hardware Speed Option for dX3-QSFP-DD-8
HWO-DX3-QSFP-DD-8-50G	50G Hardware Speed Option for dX3-QSFP-DD-8
HWO-DX3-QSFP-DD-8-PORT	dX3-QSFP-DD-8 Single Port Upgrade
HWO-DX3-QSFP-DD-8-NRZ100G	100G NRZ Hardware Speed Option for dX3-QSFP-DD-8
HWO-DX3-QSFP-DD-8-NRZ50G	50G NRZ Hardware Speed Option for dX3-QSFP-DD-8
HWO-DX3-QSFP-DD-8-NRZ40G	40G NRZ Hardware Speed Option for dX3-QSFP-DD-8
HWO-DX3-QSFP-DD-8-NRZ25G	25G NRZ Hardware Speed Option for dX3-QSFP-DD-8
HWO-DX3-QSFP-DD-8-NRZ10G	10G NRZ Hardware Speed Option for dX3-QSFP-DD-8

Ordering Information continued

Part Number	Description
UPG-NRZ-DX3-QSFP-DD-8 ¹	Factory Upgrade NRZ QSFP-DD-8-Port
HWO-DX3-QSFP-DD-8-4QSFP28 ²	Hardware Option For DX3-QSFP-DD-8 400G to 4XQSFP28
ACC-1067A	Active Copper Breakout QSFP-DD to 4XQSFP28 3M

Requirements

- Windows-based workstation with 10/100/1000 Mbps Ethernet NIC; mouse and color monitor required for GUI operation
- Linux- or Windows-based workstation for scripting
- Mac-, Linux-, or Windows-based workstation for Rest API support

1. This is a feature license which requires 8x50G-AN/LT-compatible hardware. If hardware already supports 8x50G AN/LT, quote only requires HWO-NRZ speed options 100/50/40/25/10G listed under "Hardware Upgrades" section. If hardware does not support 8x50G AN/LT, then UPG-8x50G-ANLT-8 needs to be added to quote and appliance must be returned to factory for upgrade.
2. 100G (QSFP28) high density 32x100G license (requires ACC-1067A accessory).



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