

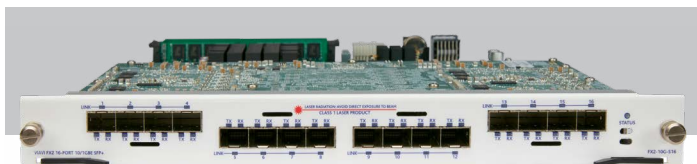
fX2 Dual-speed 16-port Module

10/1G Dual-Speed Test Modules

SFP+ 10/1G

The VIAVI fX2 10/1G Ethernet dual-speed test module for TestCenter combines VIAVI's industry-leading Layer 2-3 traffic generation and analysis with powerful network emulation and application layer protocols for emulating a wide range of device types, users and protocols. This module delivers the highest performance per dollar for Layer 2-3 testing. Reduced power consumption and the ability to use a single module throughout the test lifecycle result in lower CapEx and OpEx costs. This module is ideal for functional, conformance and performance testing of data center and service provider network infrastructure as well as evolving SDN and NFV technologies.

The VIAVI fX2 module is available in several port count and speed variations to match your test needs and budget. Dual speed modules support 10/1G operation from a single port.



VIAVI fX2

Application

- SDN and Data Center: validate forwarding performance and functional capabilities of Software Define Networks (SDN) with ultra-low latency and high port density. Supports key technologies like VXLAN, OpenFlow, and FCoE
- Device Benchmarking: test using IETF RFC 2544, RFC 2889 and RFC 3918 methodologies with easy test setup using dynamically bound traffic and automated wizards
- Core and Edge Routers and Switches: verify scale, reliability, performance of Layer 2 & 3 services including data, multicast and video delivered via unicast routing, multicast routing, switching and MPLS VPN technologies
- Carrier Ethernet: verify scale, reliability, performance of Ethernet services delivered via Ethernet OAM, MPLS-TP, VPLS, PWE3 Psuedowires, bridged Ethernet, packet transport protocols or combinations of these technologies
- Subscriber Emulation: verify setup and teardown of thousands of access subscribers using different services over various tunneling technologies (VLAN, L2GRE, MPLS, VPNs, VPLS, etc.) under normal or exceptional traffic conditions
- Functional, Conformance and Performance Testing: validate features, conformance to standards and measure system performance. Multiple port count versions meet your density and cost needs

The VIAVI fX2 10/1G Ethernet dual-speed test module delivers the highest density and lowest total cost of ownership in its class.

VIAVI's Layer 2-3 traffic generation and analysis is combined with powerful network emulation and application traffic to deliver the perfect blend of realism, scalability and performance required to test today's networks

Now with MACsec supported.



Features and Benefits

- High-density 16-port 10G with dual speed 10G/1G modes supported that offers a highly scalable test platform
- 10/1G Ethernet versions are the only high-density dual-speed modules of their type
 - Ports are software controlled for operation in 10G mode or 1G mode and are individually reservable
- Uses SFP+/SFP form-factor for more flexible 10G and 1G interconnect options
- Low total cost of ownership compared to other test modules in its class:
 - Excellent price-performance ratio that delivers faster time-to-market by combining leading-edge technical innovation with VIAVI's extensive testing experience
 - Intelligent power control to shut down unused test modules and allows faster boot time to bring capacity back on-line quickly (software update expected 2H'15)
 - More total throughput than the competition for a given power footprint
 - Enhanced chassis software license value—Two to four times the device or end-user emulation per chassis with no increase in software costs
 - Topology emulation lowers Capex by eliminating the need for multiple DUTs in multi-protocol tests
 - Intelligent results get answers in a fraction of the test time required by competitive products
 - Faster boot and firmware upgrade times mean less downtime in continuous running 24x7 regression test beds
- TestCenter's industry-leading Layer 2-3 feature set:
 - "Hardened" system already proven for testing from a single port up to 2,100 ports
 - Stress ASIC and backplane designs with live traffic changes. The number of emulated devices, the traffic they emanate and the rate at which they send it can all be changed "on the fly" making for more realistic tests and faster troubleshooting
 - Best-in-industry for measuring ultra-low sub-microsecond latencies with 10ns precision and 2.5ns resolution
- 19 different scheduling algorithms available for finding the right traffic to emulate the real-world or tax the device's ability to handle any traffic pattern—from micro-bursts to carefully timed sequences of "killer" frames

- fX2 module support TestCenter’s deep analysis system
 - Port counts, rates, errors and protocol summaries provide a high-level view for quick drill-down to specific issues
 - Broadest set of per stream metrics with simultaneous control and data plane results allows most tests to be run in a single pass
 - Real-time traffic filters allow analysis down to specific fields. Multiple metrics can be simultaneously collected and instantly analyzed
 - Dynamic views feature multi-metric extraction, sorting and operation in real-time or post test
 - Full packet capture enables timing, sequencing, and content analysis for individual packets. Powerful filters ensure the capture buffer is filled with relevant data

Technical Specifications

Product Feature	Description			
fX2 Module Specifications				
Maximum Port Density	Speed	Maximum ports per slot	Maximum ports per STP-N12U chassis	Maximum ports per SPT-N4U chassis
fX2-10G-S16 Supports Dual Speed 10/1g	10/1G	16	192	32
Media Support See Accessory Table Below for Part Numbers	<ul style="list-style-type: none"> • 10GBASE-CR (with Clause 73 Auto-Negotiation and Link Training) • Direct Attach Copper Cable • 10GBASE-SR • 10GBASE-LR • 1000BASE-SX • 1000BASE-LX • 1000BASE-T 			
Line clocking and packet time stamping – fX2 modules get their transmit line clocking and time-stamping from the control modules on the SPT-N12U and SPT-N4U	<ul style="list-style-type: none"> • Stratum-3 rated oscillator is the default time source. Transmit line clock is at the precise nominal Ethernet rate +/- < 1 PPM on initial shipment. Accurate to +/- 4.6 PPM over 15 years of operation. • Frame time stamp resolution of 2.5ns • GPS and CDMA-based external time sources are supported • IEEE 1588v2 and NTP packet-based external time sources are supported • TIA/EIA-95B-based external time sources are supported 			

Technical Specifications continued

Product Feature	Description
Inter-Module and Inter-chassis Time Synchronization	<p>Modules in the same chassis are phase-locked to the timing source of the control module. For modules in separate chassis:</p> <ul style="list-style-type: none"> • VIAVI-patented self-calibrating inter-chassis timing chain using dedicated port on chassis control module delivers precise synchronization +/- 20ns • Synchronized via external GPS or CDMA network • Using IEEE 1588 or NTP packet-based approaches • With TIA/EIA-95B timing inputs
Transmit Clock Adjustment	10/1G: +/- 102 PPM in 1 PPM increments per 4 ports (1-4, 5-8, 9-12)
Port Cpu	High-performance, server-class, stackable multi-core CPU
Processor Resource Aggregation (User-defined Aggregation)	TBD
User Reservation	Per 10G or 10/1G port
Module Weight	Q5 versions: 2.65 kg. S8 versions: 2.05 kg
Module Predicted Mtbf	Q5 versions = 35,133 hours, S8 versions = 56,330 hours, hours of continuous operation
Operating Temperature Range	<ul style="list-style-type: none"> • Q5 modules supported for 59° to 86° F (15° to 30° C) ambient temperature • All other fx2 modules are supported for 59° to 95° F (15° to 35° C) ambient temperature • 20% to 80% relative humidity
Max Power Draw per Module	Maximum of 430 W per slot

TestCenter Layer 2-3 Traffic Generation

Transmit Streams per Port (Arbitrary Values)	64K
Stream Block Definitions per Port	512 stream block definitions each capable of generating multiple streams
Frame Templates per Port	256 unique frame templates can be transmitted from each port
Transmit Statistics per Port	<ul style="list-style-type: none"> • Nearly 50 transmit stats per port reported in real time • Stats include Layer 1, Layer 2 and Layer 3 counters and rates and include counts for frames generated with CRC errors and checksum errors
Transmit Statistics per Stream	Tx frame count and rate – all Tx statistics accurate even with random frame sizes and rates
Error and Fault Generation	Link fault signaling and streamblock FCS error and IP checksum errors

Technical Specifications continued

Product Feature	Description
Variable Field Definition (Vfd) per Port	256 VFD indices per port each with 6 VFDs
Vfds per Stream	6 VFDs per stream
Route Insertion Table (Rit) Entries per Port	8M 4-byte entries for dynamic label or random IP/MAC address assignments
Rit Or List Vfd Entries per Stream	8 RIT insertions or List VFD insertions per stream
Frame Length Range	100% line rate for frames of 58-16383 bytes. Subline rate for frames from 33-57 bytes
Frame Length Controls	Fixed, increment, decrement, random, automatic based on user frame, IMIX w/ weighting for 4 nodes
Frame Rate Minimum and Maximum at Wire Rate	1 every 3.43s to 102% of line rate
Scheduler Mode Support	Port Based: traffic scheduling handled at the port level Rate Based: key parameters determined at the port level with division among the individual stream blocks Priority Based: scheduling determined at the stream block level using user-assigned priorities. Precise scheduling of CBR and bursty traffic for QoS testing. Manual Mode: manual control of stream sequence
Priority Flow Control	<ul style="list-style-type: none"> • Generator supports up to 8 queues for responding to PFC pause frames • Queue support can be integrated with DCBX emulation for automatic setup • PFC Pause frames can be sent manually for DUT response testing or triggered automatically based on configurable received traffic behavior

TestCenter Layer 2-3 Traffic Analysis

Trackable Streams per Port	128K
Statistics per Stream	<p>Over 40 real-time measurements per stream – includes standard frame and packet counters and rates and advanced sequence checking, RFC 4689 jitter, latency, FCS errors and checksum errors.</p> <ul style="list-style-type: none"> • Advanced sequencing: In-order, lost, reordered, late and duplicate • Latency: Avg, min, max and short-term avg; first/last frame arrival timestamp • Data integrity: IP checksum, TCP/UDP checksum, frame CRC, embedded CRC and PRBS bit errors

Technical Specifications continued

Product Feature	Description
Statistics per Port	<ul style="list-style-type: none"> Over 50 transmit stats per port reported in real time. Stats include Layer 1, Layer 2 and Layer 3+ counters and rates and include received FCS, checksum, and PRBS errors and rates. Also available are per-priority level PFC counters and six user-defined (pattern match) counters. Protocol port counters available for tracking key protocol messages and state information for Routing and MPLS, Carrier Ethernet, GRE, ARP and PFC control plane
TestCenter Layer 2-3 Traffic Analysis (Cont'd)	
User-defined Statistics per Port	Six user-defined statistics (count and rate for each) specified by regular expression (using AND, OR and NOT) consisting of byte pattern and offset match and/or frame length range match
Analyzer real-time filtering – identify, display and filter by user-configurable protocol field values and ranges.	<ul style="list-style-type: none"> Four 16-bit and one 32-bit analyzer filters available per-port for real-time stream analysis of test signature and non-test signature traffic Filters can be placed over protocol fields with masks and ranges to isolate specific types of traffic and by quality of service values such as: <ul style="list-style-type: none"> Transmit stream ID, IPv4/v6 SA/DA, MAC SA/DA, IP TOS/DiffServ, TCP/UDP port, VLAN ID, VLAN priority, MPLS label, MPLS exp plus more
Capture Buffer Size	256MB per port in 10G mode
Capture buffer controls – TestCenter's unique capture capability allows maximum effectiveness when debugging hard to find hardware or protocol problems.	<ul style="list-style-type: none"> Several modes of operation that include: <ul style="list-style-type: none"> Filter by protocol fields, filter by byte offset and range; store slices or full-frames; store signature or all frames; store tx/rx control plane with data plane; real-time mode for control plane traffic; wrap or stop buffer at end User-defined pattern definitions can logically combine 8 filters of up to 32 total bytes Patterns can be applied to start, filter (quality) or stop capture In addition to user-patterns, filtering, starting, and stopping capture contains the following pre-defined events: <ul style="list-style-type: none"> FCS, PRBS, IPv4 checksum, TCP/UDP/IGMP checksum, and sequence errors; undersize, oversize, jumbo, and user-defined frame length; IPv4, IPv6, TCP, UDP and IGMP packets; test signature present and test stream ID match Each event can be independently set to ignore, include or exclude
Priority Flow Control	<ul style="list-style-type: none"> Per-priority measurements for Xon response time PFC transmit time and post-PFC receive time

Technical Specifications continued

Product Feature	Description
Latency Modes	Benchmark tests support LIFO, LIFO, FIFO or FILO latency calculation methods
High-resolution sampling – high-resolution sampling and charting available for selected port or stream-block counters. Allows detailed analysis of events happening at the millisecond level (e.g., fail-over and re-route performance analysis)	<ul style="list-style-type: none"> • Available on any receive port or streamblock frame/bit/byte counter or rate • 1000 samples available at intervals of 1-100ms • Sample trigger set by relational operator of user-defined value of sampled statistic • User-defined trigger location within buffer
Histograms	Port-level histograms
TestCenter Protocol Emulation	
TestCenter protocols available as separately licensed packages. Below is a sample list of supported protocols. Contact VIAVI for a full list of capabilities and packages	
Enterprise and Data Center Switch Protocol Support	<ul style="list-style-type: none"> • OpenFlow 1.3/1.0: OpenFlow switch and controller emulation and switch conformance testing • Routing, multicast, and bridging: All major IPv4 and IPv6 unicast and multicast routing protocols, IGMPv1/v2/v3, MLDv1/v2, LACP, STP, RSTP and MSTP • Data center: DCBX, FCoE, FIP, 802.1Qbb
Service Provider Protocol Support	<ul style="list-style-type: none"> • SDN/NFV: PCE and Segment Routing • Routing and MPLS: All major IPv4 and IPv6 unicast and multicast routing protocols, RSVP-TE, LDP, VPLS-LDP, VPLS-BGP, BGP/MPLS-VPN, Fast Re-route, EVPN, mVPN, P2MP-TE, BFD, TWAMP and PWE3 (RFC4447) • Access: ANCP, PPPoE, DHCP, L2TP, IGMPv1/v2/v3, MLDv1/v2, DHCPv6 and PPPoEv6 • Carrier Ethernet and bridging: LACP, STP, RSTP and MSTP, 802.1ag CFM, Y.1731, PBB, PBB-TE, Link OAM • Mobile Backhaul: MPLS-TP, 1588v2 and Synchronous Ethernet as supported protocols
Automotive Applications	<ul style="list-style-type: none"> • 1588v2 and 8021: Secure traffic with MACsec on fX2-1G and 10G for in-vehicle network automotive applications.

Ordering Information

Part Number	Description
Test Module	
FX2-10G-S16	VIAVI fX2 10/1G SFP+ 16-ports
Accessories for SFP+ Interfaces	
ACC-6050A	Optical transceiver SFP+ MSA, 10GbE, 10GBASE-SR, MMF
ACC-6051A	Optical transceiver SFP+ MSA, 10GbE, 10GBASE-LR, SMF
ACC-6081A	Optical transceiver, SFP+ dual-rate, 10 G-1 G, 850NM, MMF
ACC-6082A	Optical transceiver SFP+ dual-rate, 10 G-1 G, 1310NM, SMF
ACC-6092A	Copper transceiver, SFP, 1000BASE-T RJ-45
ACC-7001A	Copper transceiver 10GBASE-T SFP+, RJ45 connector, 30M
AUTO-SW0-MACsec-FX2	MACsec software on FX2
VIAVI Chassis	
SPT-N12U-110	VIAVI N12U chassis and controller with 110 V AC power supplies
SPT-N12U-220	VIAVI N12U chassis and controller with 220 V AC power supplies
SPT-N4U-110	VIAVI N4U chassis and controller with 110 V AC power supplies
SPT-N4U-220	VIAVI N4U chassis and controller with 220 V AC power supplies



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