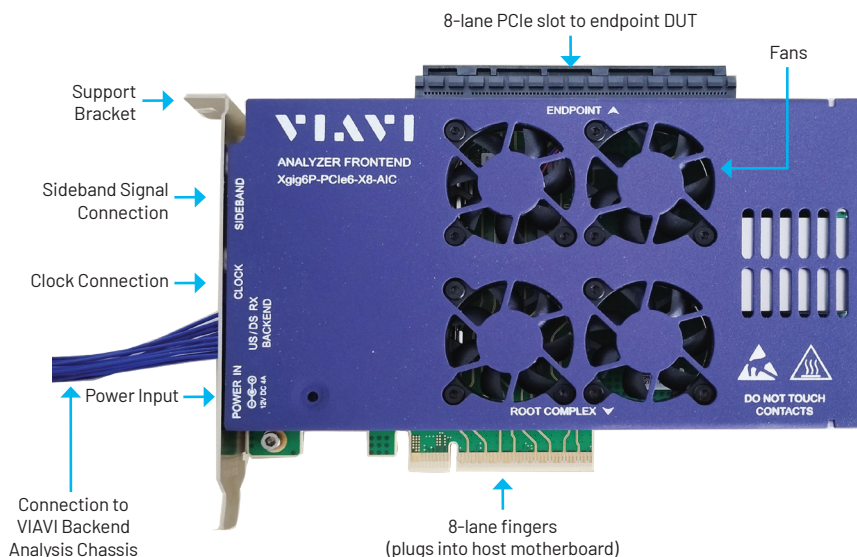


Xgig 8-lane CEM Analyzer Interposer for PCI Express® 6.0

Provides a high-fidelity tap of data signals between a host system and adapter card for the VIAVI Analyzer

VIAVI Solutions Xgig® 8-lane CEM slot Analyzer Interposer provides connection between the VIAVI PCIe 6.0 Analyzer/ Exerciser chassis and a system under test for 64GT/s x8 capture. The Analyzer Interposer installs into an 8-lane PCIe CEM slot to provide a fully analog signal path between the Root Complex and the Endpoint. The Analyzer Interposer provides a tap of the data signal communication between host and endpoint devices for input to the Analyzer/ Exerciser chassis. It operates at the 64GT/s, PCIe 6.0 rate, across 8-lanes (bi-directional).

This Analyzer Interposer uses high-speed linear signal redrivers to buffer system data signals across the Analyzer Interposer between the PCIe slot and PCB fingers. This design ensures a clean signal is delivered to the Analyzer/ Exerciser chassis and DUT devices for reliable protocol capture and analysis.



Key Features

- Supports link widths up to 8-lanes, 64GT/s capture when paired with VIAVI PCIe 6.0 x16 Analyzer/Exerciser Chassis
- Operates at 64GT/s PCIe 6.0 data rates, and is compatible with PCIe rates of 2.5, 5.0, 8.0, 16GT/s and 32GT/s
- A top edge 8-lane CEM slot connector accepts all endpoint DUT adapter cards
- Bottom edge 8-lane PCB finger connector installs into host test system's 8-lane PCIe slot
- Supplied with high-quality custom data cables for Chassis attachment to ensure good signal capture with minimal tuning effort
- Supports Analyzer side-band signal triggering and capture
- Analyzer Interposer power is independent of host system
- Format: Design is based on the 8-lane Card Electro-Mechanical (CEM) specification
- Supported by Xgig tool suite including Trace Control, Expert™ and Serialytics™
- Provides consistent, repeatable capture of link training, equalization negotiation and other data

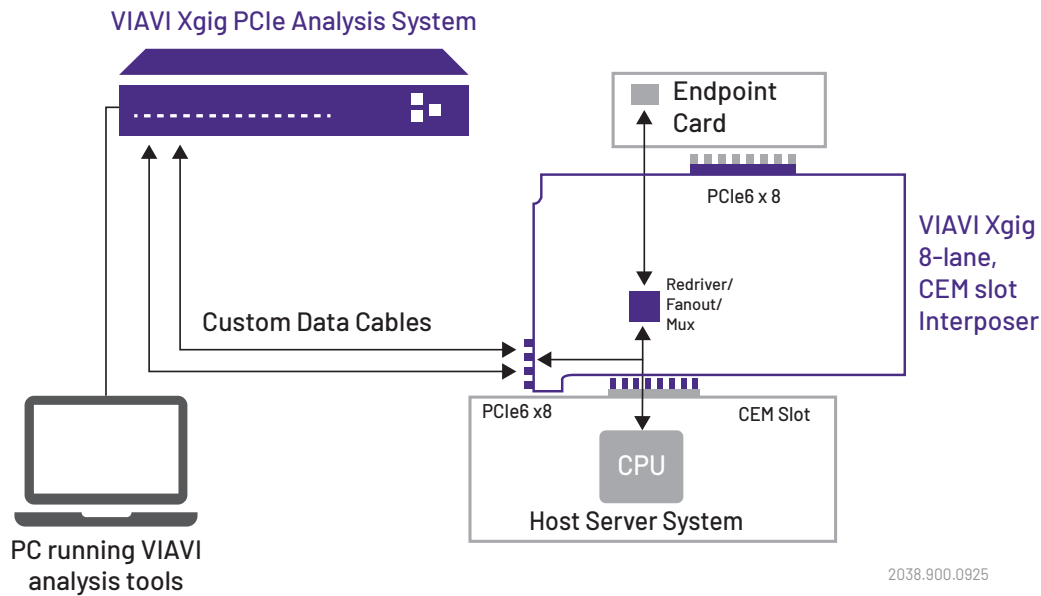
Applications

Working together with the VIAVI PCIe 6.0, Analyzer/Exerciser chassis, this Analyzer Interposer enables debug and verification of new system hardware designs, FPGA firmware, validation of system BIOS and software, and supports manufacturing test.

The Xgig 8-lane, CEM slot Analyzer Interposer is inserted into a 8-lane PCIe slot in the host system, and an endpoint card is installed into the top 8-lane slot connector creating the test data path. Cables connect the Interposer to the VIAVI protocol Analyzer/Exerciser chassis.

The diagram below indicates the signal path with DUT devices attached. Note that this Analyzer Interposer requires an open DUT system chassis while in use. The Analyzer captures DUT data (tapped) from both the upstream and downstream signal paths as inputs to the Analyzer/Exerciser chassis.

VIAVI offers a variety of other Interposer types for connecting into many popular PCIe system applications, including other CEM, U.2, M.2, EDSFF, CDFP (CoppoLink), MCIO (CoppoLink), Flying-Lead and others.



Specifications

Temperature	
Operational	-15 to +35° C
Non-operational	-40 to +70° C
Humidity	
Operational	Up to 85% humidity (non-condensing) at +35° C
Non-operational	Up to 85% humidity at +65° C
Size	180.4 mm deep x 105.1 mm tall x 18.28 mm thick
Electromagnetic Compliance	FCC part 15 Class A, CE Compatibility
Altitude for operation	Storage 2000 m above sea level (max)
Altitude for transport	12,000 m above sea level (max)
Environment Pollution	Degree 2
Power	A 120/220 AC in, 12V/3A DC output converter is included

Kit Contents

Item	Description	Qty
1	Xgig6P-PCIE6-X8-AICF, 8-lane CEM Analyzer Interposer Front End Module with cables	1
2	Sideband cable for PCIe	1
3	Power supply, 120/220 input, 12V	1
4	DUT Mounting Bracket	1
5	Quick Start Guide	1

Ordering Information

Part Number	Description
XGIG6P-PCIE6-X8-AIC	CEM Front End and Back End Modules Bundle
XGIG-INTPSR-C-Hx(x=1,2,3,4)	x-year extended hardware warranty



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