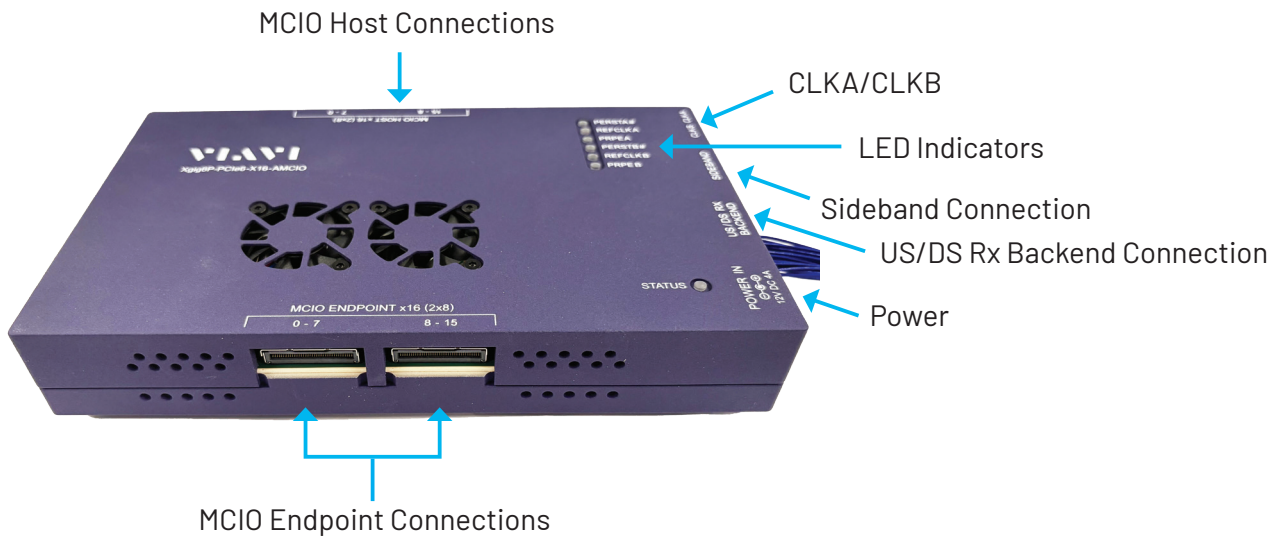


Xgig MCIO 16-lane Interposer for PCI Express® 6.0

Provides a tap of MCIO/CopprLink™ PCIe signals for input to the VIAVI PCIe 6.0 Analyzer

VIAVI Solutions Xgig® MCIO Interposer, the Xgig6P-PCIe6-X16-AMICO, supports analysis of PCIe6 communication across an MCIO/CopprLink internal connection. This Interposer enables protocol analysis of communication between a 16-lane MCIO host port and an MCIO attached endpoint device. Bidirectional PCIe 6.0 protocol data on each of 16 lanes is captured at up to 64GTps rate for analysis. High-performance, low-loss, custom cables are supplied for connection into the VIAVI Analyzer.

This Interposer uses high-speed linear signal redrivers to buffer the system data signals ensuring a clean signal is delivered to both the Analyzer and the devices-under-test (DUT) for accurate protocol capture and analysis.



Side View with Power, US/DS, Sideband and CLK Connections



Side View with MCIO Host Connections



Bottom View

Key Features

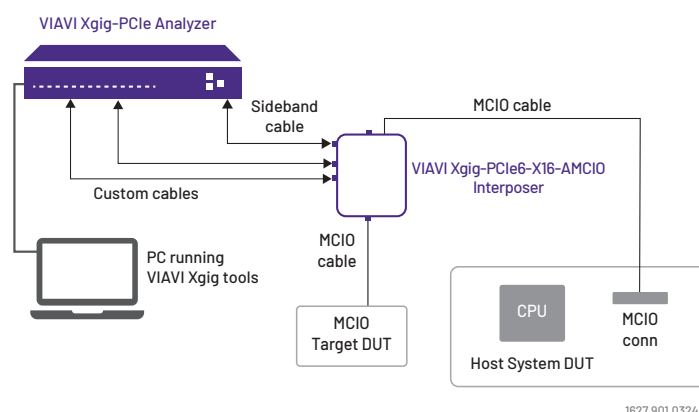
- Operates at 64GT/s PCIe 6.0 data rates, and is compatible with PCIe rates of 2.5, 5.0, 8.0, 16 and 32GT/s
- Two, 74 pin to 74 pin MCIO connectors on each side of the interposer pod support PCIe links of up to 16-lanes
- Supplied with high-quality custom cables for Analyzer attachment
- Data path uses high-speed linear redrivers to ensure good signal capture with minimal tuning
- Supports Analyzer side-band signal capture and triggering via custom cable
- Cables supplied for optional clocking sources
- Interposer power is independent of host system
- LEDs give quick indication of power and operation
- Provides consistent, repeatable capture of link training, equalization negotiation and other data
- Up to two Interposers are stackable to support wider length widths
- Supported by VIAVI Xgig tool suite including Trace Control, Expert™ and Serialytics™

Applications

Working together with the VIAVI PCIe 6.0 Analyzer/ Exerciser platform, the Interposer enables debug and validation of new ICs with MCIO interface, MCIO peripherals, host system implementations, firmware and drivers, validation of system BIOS and software, error injection, and for manufacturing test. It supports full decode and analysis of PCIe data traffic and link states.

To install the Xgig6P-PCIe6-X16-AMC10 Interposer, with the system power-off, connect the Interposer into the MCIO cable path and to the Analyzer chassis using the supplied MCIO or MXIO/CopprLink internal cables. Power-on the Interposer and Analyzer, specify the trace capture conditions using the Xgig tools and then power-up the system under test.

The block diagram at right indicates the signal data path. Data flows between the host system's MCIO interface and the MCIO target DUT as normal. The signal is "tapped" for use by the Analyzer inside the Interposer.



When operating in Analyzer mode, the DUT data is captured (tapped) from both the upstream and downstream data paths as inputs to the Analyzer.

In addition to this 16-lane MCIO Interposer, VIAVI offers a variety of other Interposer types for connecting into many popular PCIe system applications, including CEM, U.2, M.2, EDSFF, CDFP, OCP3, Flying-Lead and others.

Specifications

Temperature	
Operational	-15 to +35° C
Non-operational	-40 to +70° C
Humidity	
Operational	Up to 85% humidity (non-condensing) at +35° C
Non-operational	Up to 85% humidity at +65° C
Size	208 x 124 x 33 mm (L x W x H) (excl. rubber feet and built-in cables)
Electromagnetic Compliance	FCC part 15 Class A, CE Compatibility
Altitude for operation	Storage 2000 m above sea level (max)
Altitude for transport	12,000 m above sea level (max)
Environment Pollution	Degree 2
Power	A 120/220 AC in, 12V/3A DC output converter is included

Kit Contents

Item	Description	Qty
1	Xgig 16-lane MCIO Interposer for PCIe 6.0	1
2	74p to 74p MCIO (aka CopprLink™) internal cables	4
3	Custom high-performance cables	2
4	Sideband signal cable (to Analyzer)	1
5	MCX 50-Ohm coax cables for optional clock	2
6	Power brick, 120/220 input, 12V/3A output	1
7	Quick Start Guide	1

Ordering Information

Part Number	Description
XGIG6P-PCIE6-X16-AMCIO	MCIO Interposer, 16-lane, 64GTps, for PCIe 6.0
XGIG-INTPSR-C-Hx (x=1, 2, 3, 4)	x-year extended hardware warranty



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